

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A transconductance amplifier for an inductive load, comprising:

an input stage for receiving a driving signal (set-point), a power stage connected downstream of the input stage and connected to the load, ~~and an output stage~~ feedback on the input stage to transfer a signal associated with the load, the input stage comprising at least one comparator for receiving on an input the driving signal and on another input the output of the output stage, the amplifier further comprising a delay block coupled between the comparator output and the power stage.

2. (Canceled)

<sup>2</sup>  
~~2.~~ (Currently Amended) The amplifier of claim 2-1 wherein a time "d" of the delay block is adjustable and programmable.

<sup>3</sup>  
~~3.~~ (Original) The amplifier of claim 1 wherein the comparator comprises a hysteretic comparator.

<sup>4</sup>  
~~4.~~ (Currently Amended) The amplifier of claim 2-1 wherein the power stage comprises a pair of class D amplifiers connected to the load.

<sup>5</sup>  
~~5.~~ (Currently Amended) The amplifier of claim 2-1 wherein the power stage comprises a pair of amplifiers connected to the load, the one in class D and the other in class AB.

<sup>6</sup>  
~~6.~~ (Original) The amplifier of claim <sup>5</sup>~~6~~ wherein the amplifier of class AB has a gain -K.

<sup>7</sup>~~8~~. (Original) The amplifier of claim <sup>2</sup>~~8~~ wherein an adjustable time "d" of said delay block is of at least 500ns.

<sup>8</sup>~~9~~. (Currently Amended) A method for driving inductive loads, comprising:  
a driving step using a transconductance amplifier, and controlling a load current outputted from a power stage by utilizing an output of a comparator, and wherein the comparator is delayed in switching by a programmable delay.

<sup>9</sup>~~10~~. (Original) The method of claim <sup>8</sup>~~9~~ wherein the comparator comprises a hysteretic comparator.

11. (Canceled)

PN <sup>10</sup>~~12~~. (Original) A circuit, comprising:  
an inductive load; and  
a transconductance amplifier, comprising:  
an input stage comprising a comparator having a first input for receiving a driving signal and a second input for receiving a feedback signal, and an output;  
a power stage coupled to the input stage via a delay block, the power stage having an output; and  
a sensing stage coupled to the power stage and to the second input of the comparator for controlling the power stage.

<sup>11</sup>~~13~~. (Original) The circuit of claim <sup>10</sup>~~12~~ wherein the input stage comprises at least one comparator.

<sup>12</sup>~~14~~. (Original) A transconductance amplifier for an inductive load, the amplifier comprising:  
a comparator having a first input for receiving a driving signal and a second input for receiving a feedback signal and an output;

a delay block having an input coupled to the output of the comparator and an output;

a first amplifier having an input coupled to the output of the delay block and an output configured to be coupled to the load;

a second amplifier having an input coupled to the output of the delay block via an inverter and an output;

a sense resistor having a first terminal coupled to the output of the second amplifier and a second terminal coupled to the load; and

a sense amplifier having a first input coupled to the first terminal of the sense resistor and a second input coupled to the second terminal of the sense resistor and configured to generate the feedback signal at an output coupled to the second input of the comparator.

<sup>13</sup> 15. (Original) A transconductance amplifier for an inductive load, comprising:

a hysteretic comparator having a first input for receiving a driving signal and a second input for receiving a feedback signal, and an output;

PN a first amplifier having an input coupled to the output of the hysteretic comparator and an output configured to be coupled to the load;

a second amplifier having an input coupled to the output of the hysteretic comparator via an inverter and an output;

a sense resistor having a first terminal coupled to the output of the second amplifier and a second terminal coupled to the load; and

a sense amplifier having a first input coupled to the first terminal of the sense resistor and a second input coupled to the second terminal of the sense resistor and configured to generate the feedback signal on an output coupled to the second terminal of the hysteretic comparator.

<sup>14</sup> 16. (Original) The amplifier of claim 15 wherein the first amplifier comprises a PWM class deamplifier and the second amplifier comprises a class AB linear amplifier.

<sup>15</sup>  
~~17~~ (Original) A transconductance amplifier for an inductive load, comprising:

a comparator having a first input configured to receive a driving signal and a second input configured to receive a feedback signal, and an output;

a delay block having an input coupled to the output of the comparator and further comprising an output;

a first amplifier having an input coupled to the output of the delay block and an output configured to be coupled to the load;

a second amplifier having an input coupled to the first input of the comparator and an output;

a sense resistor having a first terminal coupled to the output of the second amplifier and a second terminal configured to be coupled to the load; and

a sense amplifier having a first input coupled to the first terminal of the sense resistor and a second input coupled to the second terminal of the sense resistor and configured to generate a feedback signal on an output coupled to the second terminal of the comparator.

<sup>16</sup>  
~~18~~ (Currently Amended) A method for driving an inductive load, comprising:

receiving a signal at an input stage;

driving the signal via a power stage through the load; and

controlling a load current outputted from the power stage via the output of a comparator, wherein the comparator is configured to generate an output, and further comprising delaying the output of the comparator by a programmable delay.

<sup>17</sup>  
~~19~~ (Original) The method of claim <sup>16</sup>~~18~~ wherein the comparator comprises a hysteretic comparator.

20. (Canceled)